

YOU LI

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EDUCATION

University of Michigan

B.S.E. in Computer Engineering

Ann Arbor, MI | Sep 2026 - May 2028 (Expected)

University of Rochester

B.S. in Electrical and Computer Engineering (Dean's List)

Rochester, NY | Sep 2024 - May 2026

Relevant Coursework: Logic Design, Digital Integrated Circuits, Embedded Control Systems, Computer Organization, Semiconductor Devices, Signals and Systems, Programming and Data Structures

INTERNSHIP AND TEACHING EXPERIENCE

Eaton Corporation

Shanghai, China | Jun 2026 - Aug 2026

IT Intern - Automation & Data Tools (SQL, Python, JavaScript)

- Extended a third-party low-code platform with custom code blocks and SQL to implement company-specific workflow functions.
- Automated collection of supplier procurement and business data using Python web scraping; exported records to Excel for internal use.
- Created training videos and technical documentation for internal office automation systems.

University of Rochester

Rochester, NY | Feb 2026 - May 2026

Laboratory Teaching Intern - Electricity & Magnetism

- Supported setup and troubleshooting of digital oscilloscopes, multimeters, function generators, power supplies, and circuit components.
- Guided students through electricity and magnetism experiments covering Coulomb's law, electric fields, magnetic forces, and RC/RLC circuits.
- Helped students interpret measurements and identify experimental errors, connecting circuit behavior with electricity and magnetism concepts.

Eaton Corporation

Shanghai, China | Jun 2025 - Aug 2025

IT Intern - Office Automation (Low-code workflows)

- Developed low-code workflows for factories in Suzhou, Dongguan, Xi'an, and Ningbo, covering leave, resignation, procurement, travel, company events, and parts replacement.
- Configured multistage approval routes across employees, managers, HR, EHS, and Finance to meet factory-specific requirements.
- Tested workflows before deploying validated releases to production for factory users.
- Diagnosed workflow issues by tracing data flows and approval logic; clarified requirements with business users.

PROJECT EXPERIENCE

Smart Package Detection System (ESP32, C++, CMake/CTest, I2C, URAT)

Independent Project | Jun 2026 - Present

- Built an ESP32 prototype in C++ with a VL53L0X time-of-flight sensor over I2C; implemented a finite-state machine with EMA filtering, hysteresis, and temporal debouncing.
- Developed a Wi-Fi dashboard, HTTP/JSON API, CSV logging, and automated tests with CMake/CTest and GitHub Actions; validated three controlled tabletop arrival/removal cycles.

FPGA Digital Logic Design (Verilog, Quartus II)

University of Rochester | Feb 2025 - May 2025

- Designed and synthesized combinational and sequential circuits in Verilog using Quartus II; simulated and deployed designs to a DE0-CV FPGA via JTAG.
- Validated adders, multiplexers, display drivers, counters, and an FSM-based bitstream encryption device.

FIRST Robotics Competition - Team 8015 (roboRIO, Limelight)

Shanghai, China | Sep 2022 - Sep 2024

- Wired the roboRIO controller, power distribution components, and Limelight vision camera using robot electrical diagrams.
- Assisted with Limelight configuration and debugging, subsystem testing, and electromechanical fault isolation.

SKILLS

Programming & HDL: C/C++, Python, JavaScript, Verilog

Lab & Prototyping: Oscilloscopes, multimeters, function generators, power supplies, 0603 PCB soldering, circuit prototyping

Embedded Systems: ESP32, Arduino, I2C, UART, roboRIO, Limelight

Digital Design & Testing: DE0-CV FPGA, finite-state machines, RTL synthesis, timing simulation, regression testing

Development Tools: Quartus II, Git, GitHub Actions, CMake/CTest, VS Code